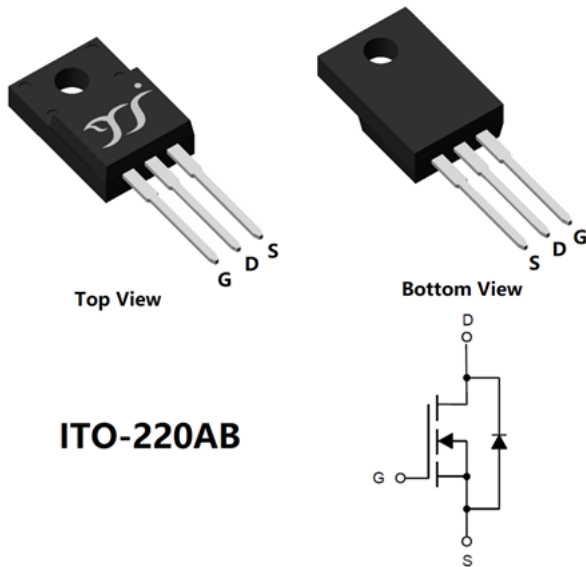


## N-Channel Enhancement Mode Field Effect Transistor



### Product Summary

- $V_{DS}$  635V
- $I_D$  7A
- $R_{DS(ON)}$  (at  $V_{GS}=10V$ )  $<700m\Omega$
- 100% EAS Tested
- 100%  $\nabla V_{DS}$  Tested

### General Description

- Super Junction High Voltage MOSFET technology
- Ultra Low Gate Charge Cause Lower Driving Requirement
- Low On-resistance and Low Conduction Loss
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

### Applications

- Power switching application
- Uninterruptible power supply
- DC-DC convertor

### Limiting Values

| Parameter                              | Conditions                                                                              |                                            | Symbol                           | Min | Max  | Unit |
|----------------------------------------|-----------------------------------------------------------------------------------------|--------------------------------------------|----------------------------------|-----|------|------|
| Drain-source Voltage                   |                                                                                         |                                            | V <sub>DS</sub>                  | -   | 635  | V    |
| Gate-source Voltage                    |                                                                                         |                                            | V <sub>GS</sub>                  | -30 | 30   |      |
| Continuous Drain Current (Note 1,2)    | Steady-State                                                                            | T <sub>A</sub> =25°C,V <sub>GS</sub> =10V  | I <sub>D</sub>                   | -   | 0.97 | A    |
|                                        |                                                                                         | T <sub>A</sub> =100°C,V <sub>GS</sub> =10V |                                  | -   | 0.61 |      |
| Continuous Drain Current (Note 1,3)    | Steady-State                                                                            | T <sub>C</sub> =25°C,V <sub>GS</sub> =10V  |                                  | -   | 7    |      |
|                                        |                                                                                         | T <sub>C</sub> =100°C,V <sub>GS</sub> =10V |                                  | -   | 4.4  |      |
| Pulsed Drain Current                   | T <sub>C</sub> =25°C,t <sub>p</sub> ≤10μs                                               |                                            | I <sub>DM</sub>                  | -   | 14   |      |
| Maximum Body-Diode Continuous Current  | T <sub>C</sub> =25°C                                                                    |                                            | I <sub>S</sub>                   |     | 7    |      |
| Maximum Body-Diode Pulsed Current      | T <sub>C</sub> =25°C,t <sub>p</sub> ≤10μs                                               |                                            | I <sub>SM</sub>                  | -   | 14   |      |
| Avalanche Energy (non-repetitive )     | T <sub>J</sub> =25°C,V <sub>G</sub> =10V,R <sub>G</sub> =25Ω,L=30mH,I <sub>AS</sub> =2A |                                            | EAS                              | -   | 60   | mJ   |
| Total Power Dissipation (Note 1,2)     | Steady-State                                                                            | T <sub>A</sub> =25°C                       | P <sub>D</sub>                   | -   | 1.78 | W    |
|                                        |                                                                                         | T <sub>A</sub> =100°C                      |                                  | -   | 0.71 |      |
| Total Power Dissipation (Note 1,3)     | Steady-State                                                                            | T <sub>C</sub> =25°C                       |                                  | -   | 29.7 |      |
|                                        |                                                                                         | T <sub>C</sub> =100°C                      |                                  | -   | 11.9 |      |
| MOSFET dv/dt Ruggedness                | V <sub>DS</sub> =0...300V,ID<=4A,Rg=0Ω                                                  |                                            | dv/dt                            | -   | 27.4 | V/ns |
| Reverse Diode dv/dt                    | V <sub>DS</sub> =0...300V,ID<=4A,di/dt=200A/us                                          |                                            | dv/dt                            | -   | 6    |      |
| Maximum Diode Commutation Speed        | V <sub>DS</sub> =0...300V,ID<=4A,Rg=0Ω                                                  |                                            | dif/dt                           | -   | 860  | A/us |
| Insulation withstand voltage           | T <sub>J</sub> =25°C,Vrms, t=60s                                                        |                                            | V <sub>ISO</sub>                 | -   | ≥4   | KV   |
| Junction and Storage Temperature Range |                                                                                         |                                            | T <sub>J</sub> ,T <sub>STG</sub> | -55 | 150  | °C   |

### Thermal Resistance

| Parameter                                       |              | Symbol          | Typ | Max | Units         |
|-------------------------------------------------|--------------|-----------------|-----|-----|---------------|
| Thermal Resistance Junction-to-Ambient (Note 2) | Steady-State | $R_{\theta JA}$ | -   | 70  | $^{\circ}C/W$ |
| Thermal Resistance Junction-to-Case             | Steady-State | $R_{\theta JC}$ | -   | 4.2 |               |

### Ordering Information (Example)

| PREFERRED P/N | PACKING CODE | Marking    | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|---------------|--------------|------------|----------------------|-------------------------|----------------------------|---------------|
| YJF700C60BHJ  | B1           | F700C60BHJ | 50                   | /                       | 5000                       | Tube          |



# YJF700C60BHJ

## ■ Electrical Characteristics

| Parameter                                    | Symbol              | Conditions                                                                                                    | Min | Typ   | Max  | Units |
|----------------------------------------------|---------------------|---------------------------------------------------------------------------------------------------------------|-----|-------|------|-------|
| Static Parameter                             |                     |                                                                                                               |     |       |      |       |
| Drain-Source Breakdown Voltage               | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA, T <sub>j</sub> =25°C                                              | 635 | -     | -    | V     |
|                                              |                     | V <sub>GS</sub> =0V, I <sub>D</sub> =1mA, T <sub>j</sub> =25°C                                                | 635 | -     | -    | V     |
| Zero Gate Voltage Drain Current              | I <sub>DSS</sub>    | V <sub>DS</sub> =635V, V <sub>GS</sub> =0V, T <sub>j</sub> =25°C                                              | -   | -     | 1    | μA    |
|                                              |                     | V <sub>DS</sub> =635V, V <sub>GS</sub> =0V, T <sub>j</sub> =150°C                                             | -   | -     | 100  |       |
| Gate-Source Leakage Current                  | I <sub>GSS</sub>    | V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V, T <sub>j</sub> =25°C                                              | -   | -     | ±100 | nA    |
| Gate Threshold Voltage                       | V <sub>GS(th)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA, T <sub>j</sub> =25°C                                | 2   | 2.9   | 3.6  | V     |
| Static Drain-Source On-Resistance            | R <sub>DS(on)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A, T <sub>j</sub> =25°C                                              | -   | 540   | 700  | mΩ    |
|                                              |                     | V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A, T <sub>j</sub> =150°C                                             | -   | 1455  | 1890 | mΩ    |
| Diode Forward Voltage                        | V <sub>SD</sub>     | I <sub>S</sub> =4.5A, V <sub>GS</sub> =0V, T <sub>j</sub> =25°C                                               | -   | 0.86  | 1.2  | V     |
| Gate Resistance                              | R <sub>G</sub>      | f=1MHz, T <sub>j</sub> =25°C                                                                                  | -   | 100   | -    | Ω     |
| Dynamic Parameters                           |                     |                                                                                                               |     |       |      |       |
| Input Capacitance                            | C <sub>iss</sub>    | V <sub>DS</sub> =300V, V <sub>GS</sub> =0V, f=1MHz, T <sub>j</sub> =25°C                                      | -   | 347   | -    | pF    |
| Output Capacitance                           | C <sub>oss</sub>    |                                                                                                               | -   | 10.3  | -    |       |
| Reverse Transfer Capacitance                 | C <sub>rss</sub>    |                                                                                                               | -   | 2.3   | -    |       |
| Effective output capacitance, energy related | C <sub>o(er)</sub>  | V <sub>DS</sub> =0...300V, V <sub>GS</sub> =0V, f=1MHz, T <sub>j</sub> =25°C                                  | -   | 19.5  | -    |       |
| Effective output capacitance, time related   | C <sub>o(tr)</sub>  |                                                                                                               | -   | 111.3 | -    |       |
| Switching Parameters                         |                     |                                                                                                               |     |       |      |       |
| Total Gate Charge                            | Q <sub>g</sub>      | V <sub>GS</sub> =10V, V <sub>DS</sub> =300V, I <sub>D</sub> =4.5A, T <sub>j</sub> =25°C                       | -   | 9.3   | -    | nC    |
| Gate-Source Charge                           | Q <sub>gs</sub>     |                                                                                                               | -   | 1.6   | -    |       |
| Gate-Drain Charge                            | Q <sub>gd</sub>     |                                                                                                               | -   | 5     | -    |       |
| Reverse Recovery Charge                      | Q <sub>rr</sub>     | I <sub>F</sub> =4.5A, di/dt=100A/μs, V <sub>GS</sub> =0V, V <sub>R</sub> =300V, T <sub>j</sub> =25°C          | -   | 1024  | -    | nC    |
| Reverse Recovery Time                        | t <sub>rr</sub>     |                                                                                                               | -   | 135   | -    | ns    |
| Peak Reverse Recovery Current                | I <sub>rrm</sub>    |                                                                                                               | -   | 13.8  | -    | A     |
| Turn-on Delay Time                           | t <sub>D(on)</sub>  | V <sub>GS</sub> =10V, V <sub>DS</sub> =300V, I <sub>D</sub> =4.5A, R <sub>GEN</sub> =3Ω, T <sub>j</sub> =25°C | -   | 42    | -    | ns    |
| Turn-on Rise Time                            | t <sub>r</sub>      |                                                                                                               | -   | 24    | -    |       |
| Turn-off Delay Time                          | t <sub>D(off)</sub> |                                                                                                               | -   | 89    | -    |       |
| Turn-off Fall Time                           | t <sub>f</sub>      |                                                                                                               | -   | 27    | -    |       |

Note:

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. The value of  $R_{\theta JA}$  is measured in the still air environment with  $T_A=25^\circ C$ . The maximum allowed junction temperature of  $150^\circ C$ .
3. Thermal resistance from junction to soldering point (on the exposed drain pad).

## ■ Typical Electrical and Thermal Characteristics Diagrams

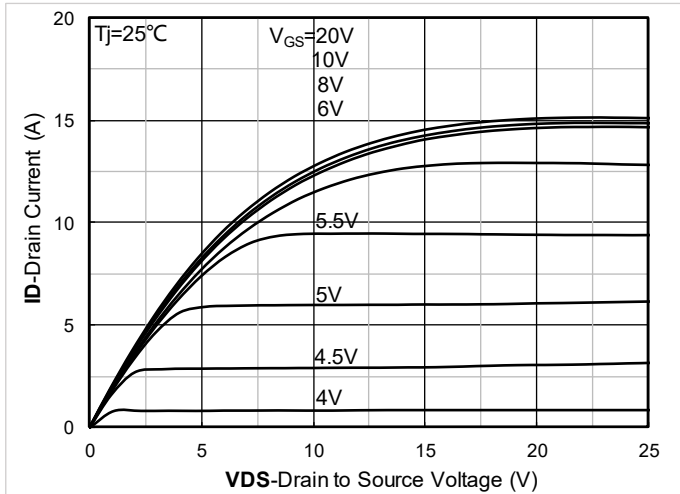


Figure 1. Output Characteristics; typical values

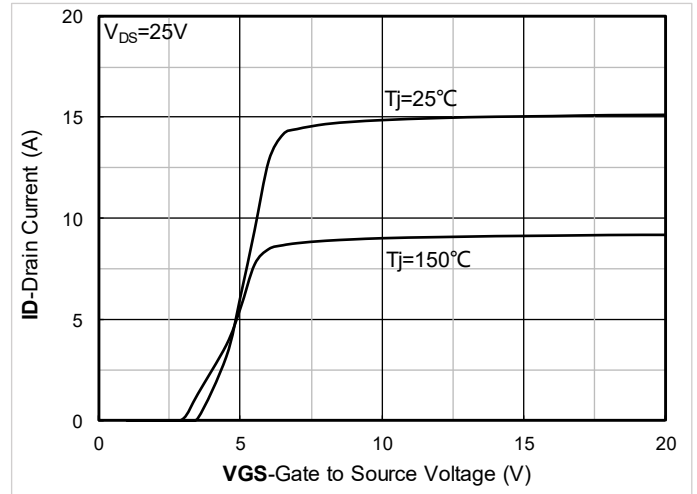


Figure 2. Transfer Characteristics; typical values

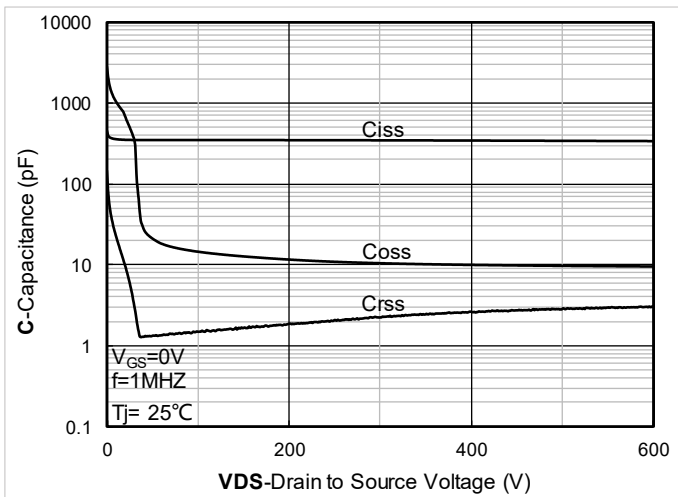


Figure 3. Capacitance Characteristics; typical values

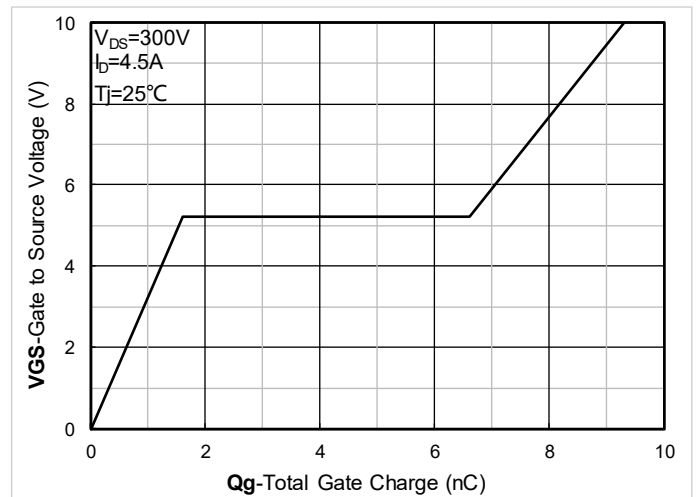


Figure 4. Gate Charge; typical values

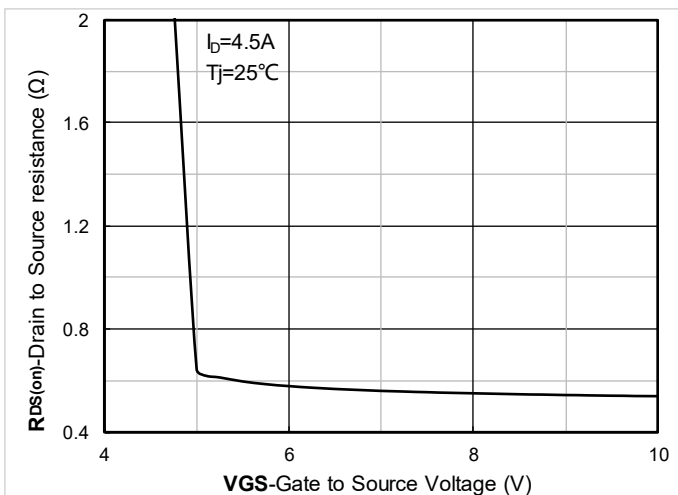


Figure 5. On-Resistance vs. Gate to Source Voltage; typical values

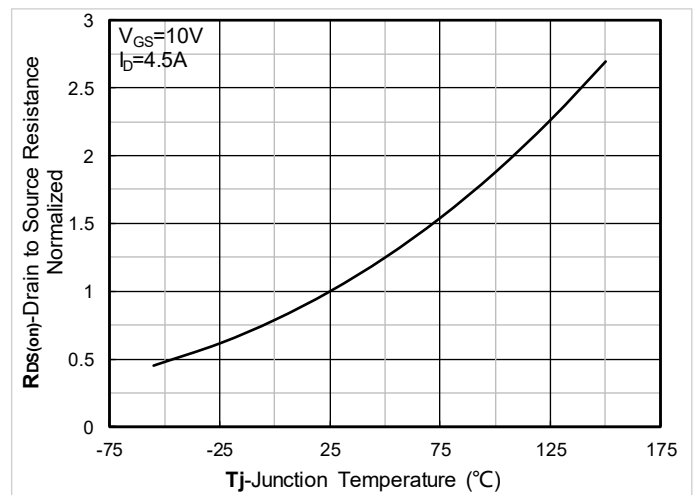


Figure 6. Normalized On-Resistance



## YJF700C60BHJ

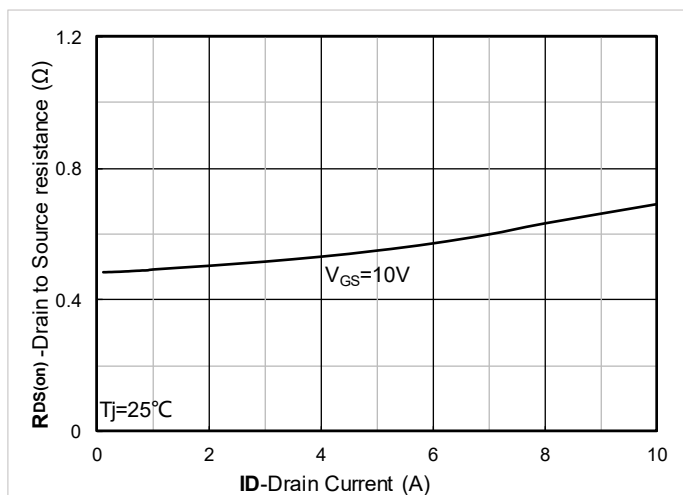


Figure 7. RDS(on) vs. Drain Current; typical values

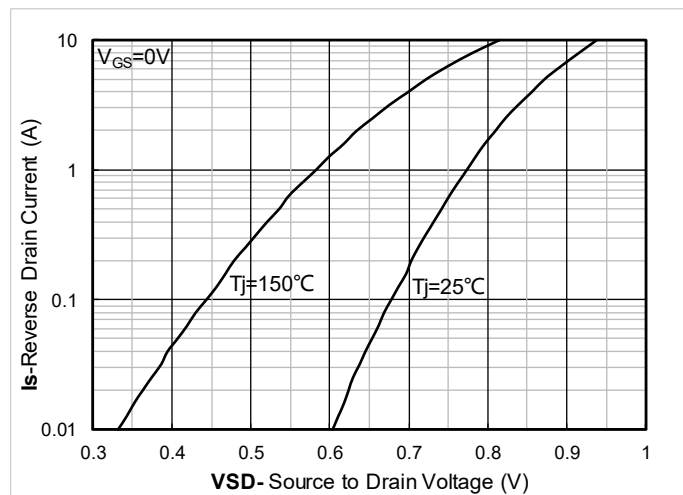


Figure 8. Forward characteristics of reverse diode; typical values

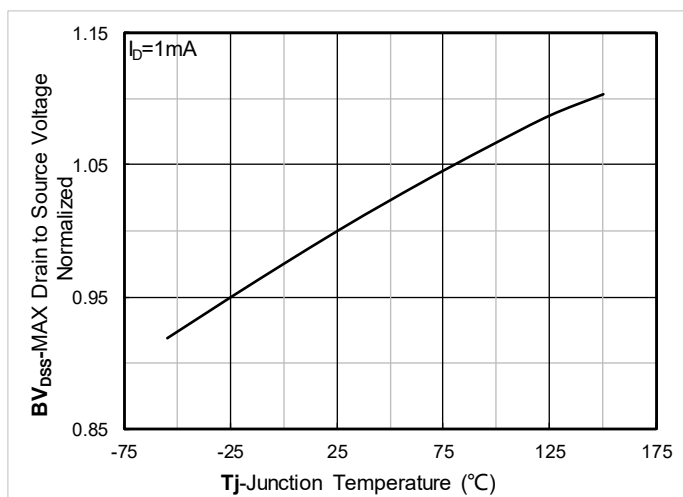


Figure 9. Normalized breakdown voltage

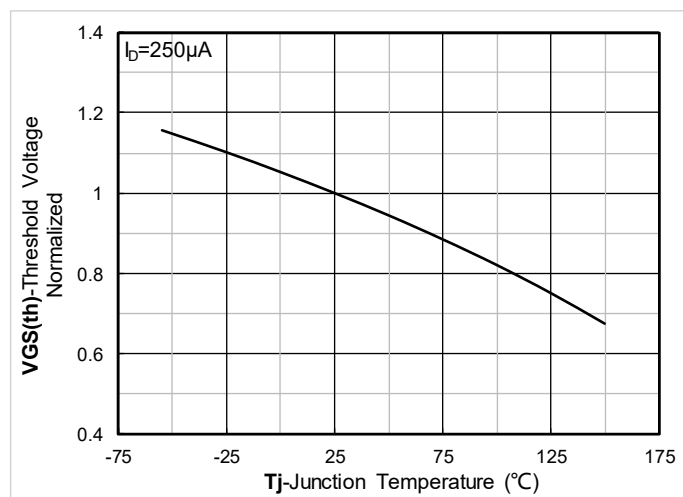


Figure 10. Normalized Threshold voltage

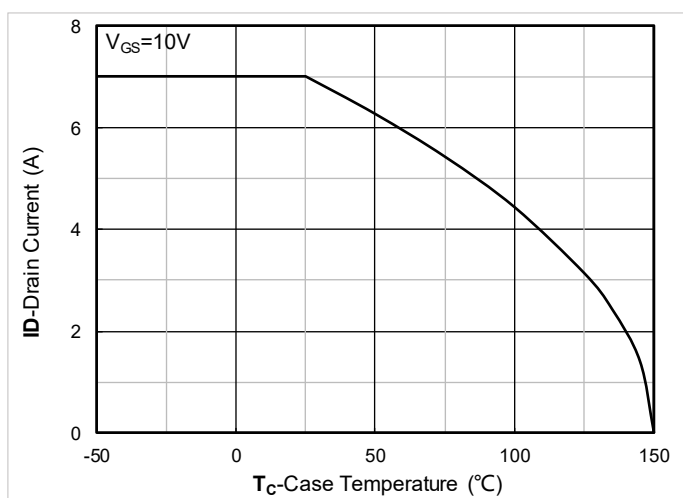


Figure 11. Current dissipation

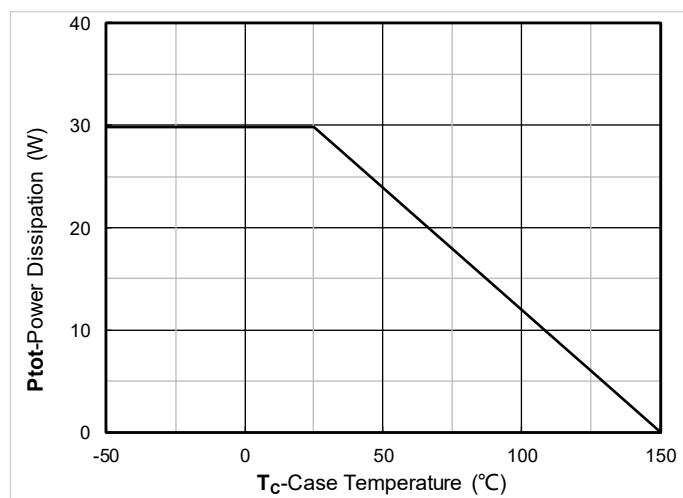


Figure 12. Power dissipation

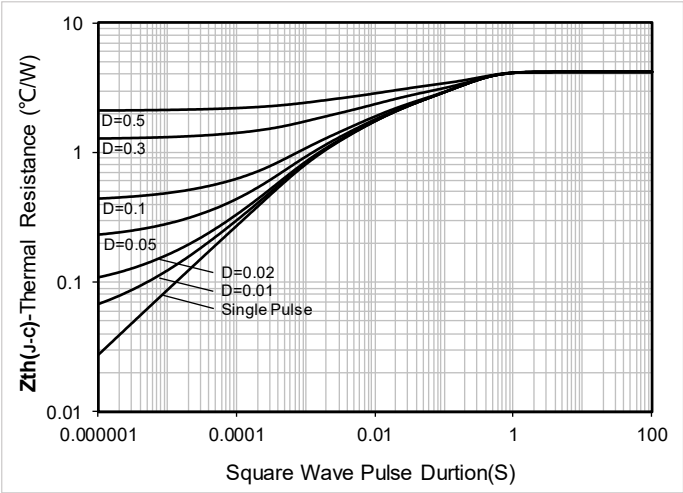


Figure 13. Maximum Transient Thermal Impedance

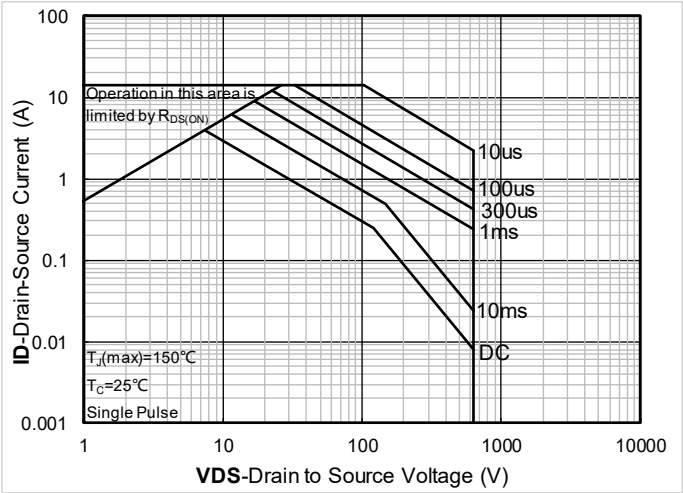


Figure 14. Safe Operation Area

## ■ Test Circuits & Waveforms

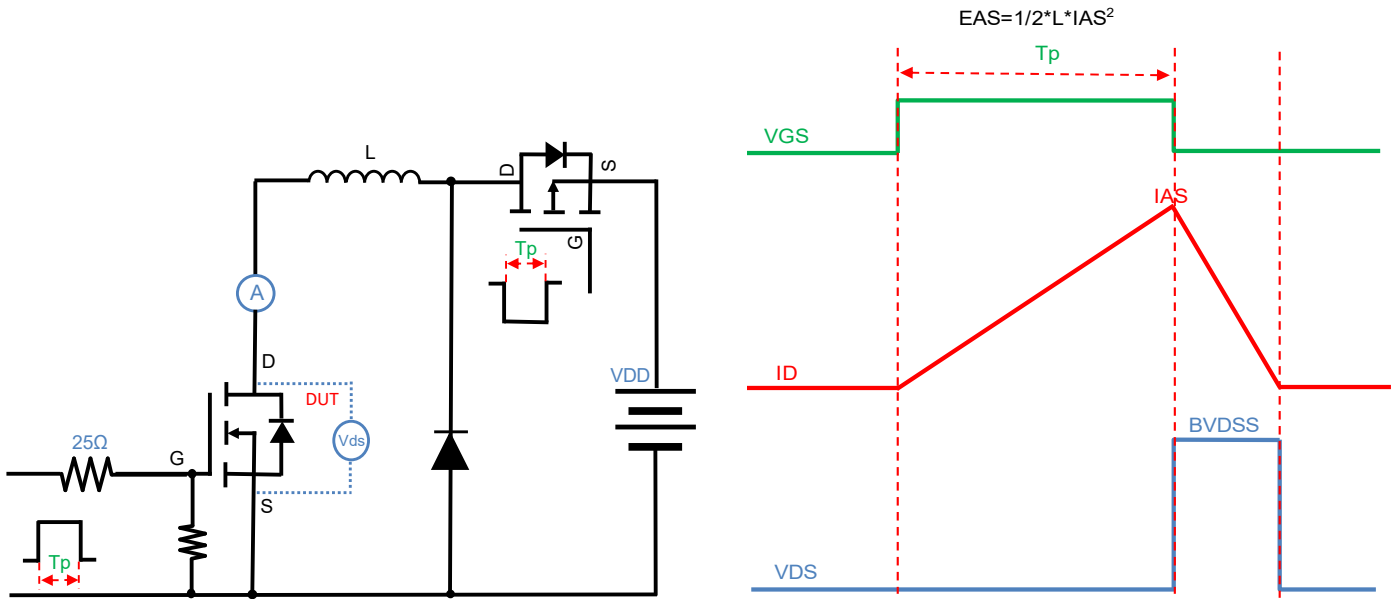


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

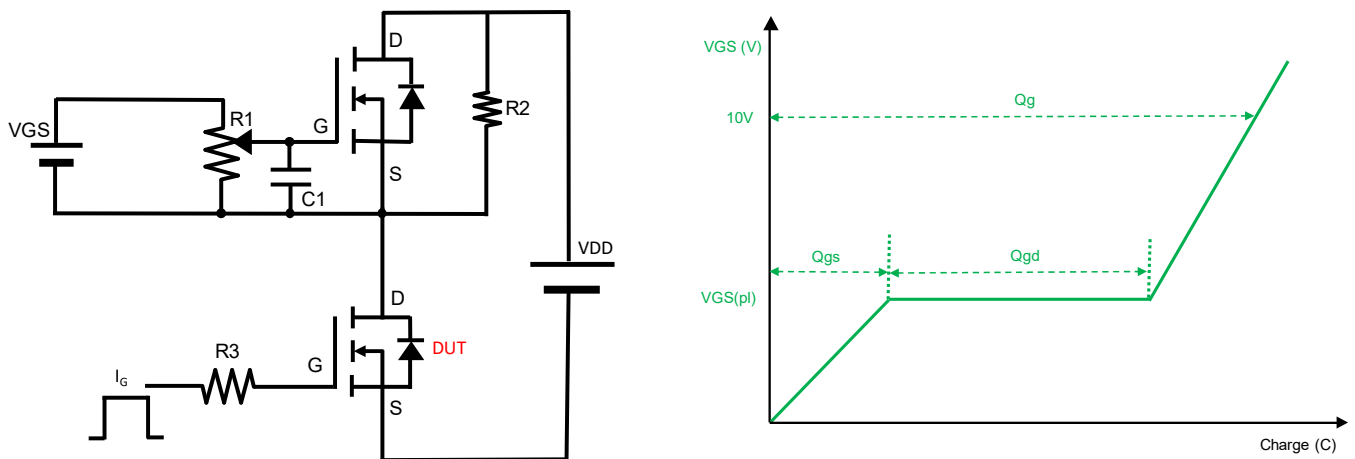


Figure B. Gate Charge Test Circuit & Waveform

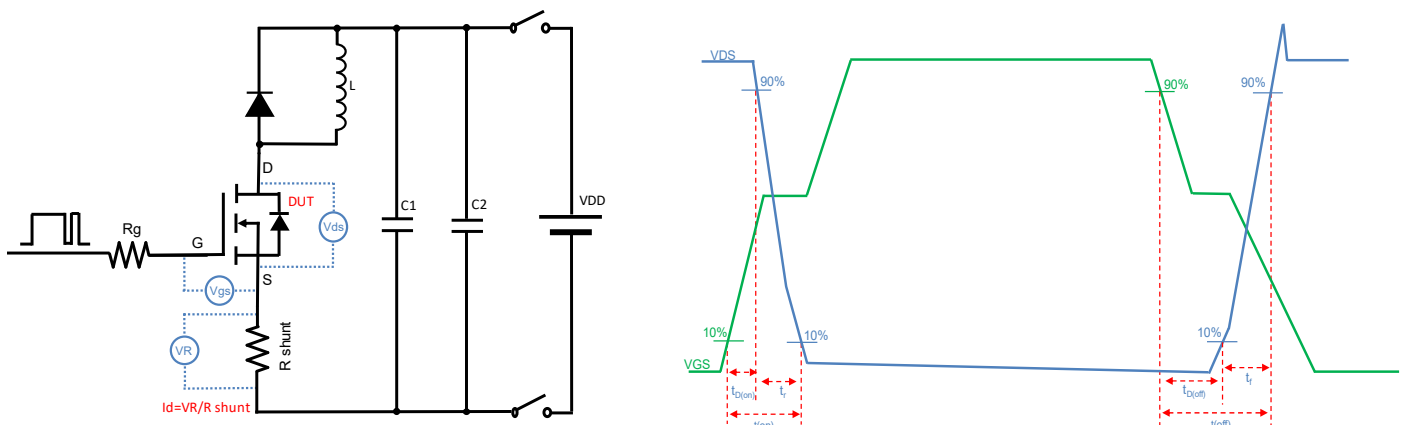


Figure C. Resistive Switching Test Circuit & Waveform

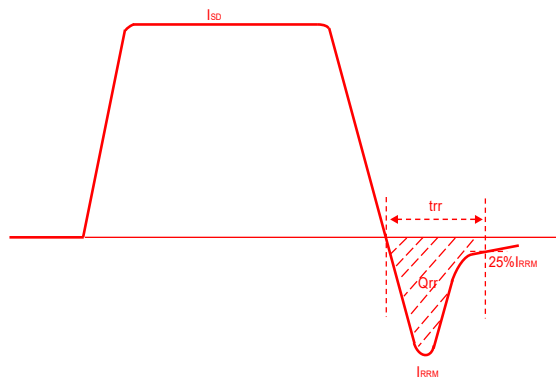
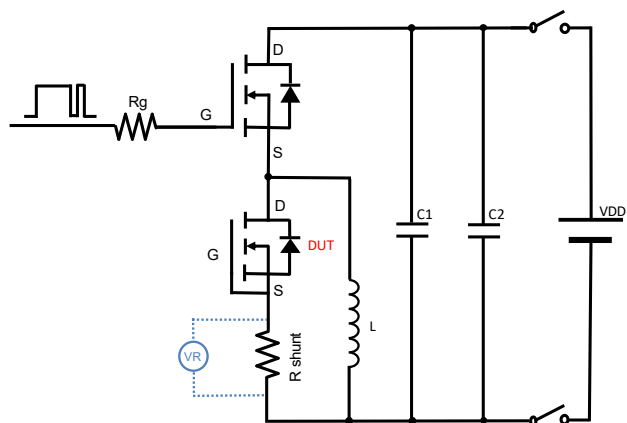
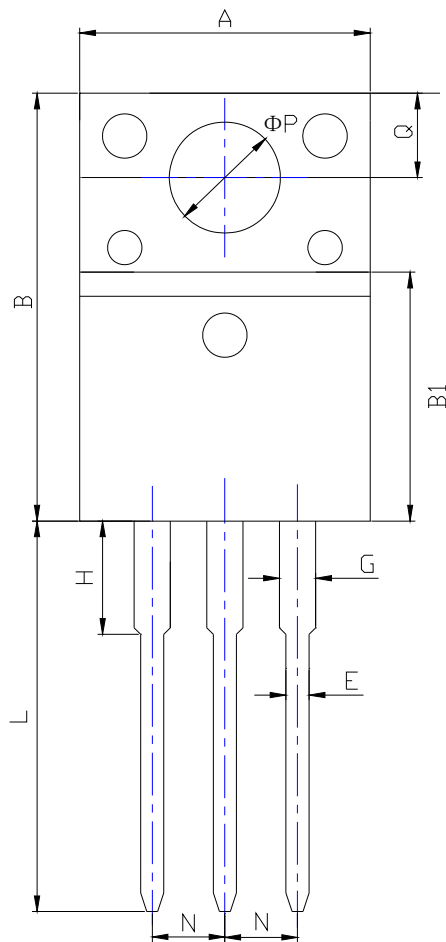


Figure D. Diode Recovery Test Circuit & Waveform

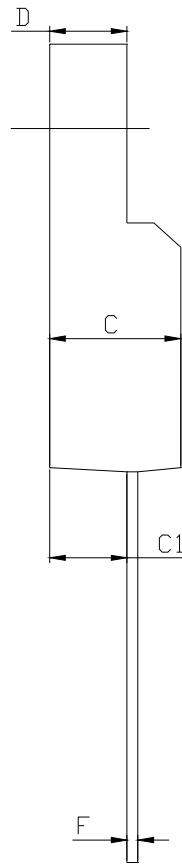


## YJF700C60BHJ

### ■ ITO-220AB-C Package information



TOP VIEW



SIDE VIEW

| SYMBOL | DIMENSIONS |       |            |        |
|--------|------------|-------|------------|--------|
|        | INCHES     |       | Millimeter |        |
|        | MIN.       | MAX.  | MIN.       | MAX.   |
| A      | 0.382      | 0.406 | 9.700      | 10.300 |
| B      | 0.610      | 0.634 | 15.500     | 16.100 |
| B1     | 0.354      | 0.370 | 8.990      | 9.390  |
| C      | 0.177      | 0.193 | 4.500      | 4.900  |
| C1     | 0.102      | 0.116 | 2.600      | 2.950  |
| D      | 0.092      | 0.108 | 2.340      | 2.740  |
| E      | 0.028      | 0.035 | 0.700      | 0.900  |
| F      | 0.016      | 0.024 | 0.400      | 0.600  |
| G      | 0.044      | 0.056 | 1.120      | 1.420  |
| H      | 0.106      | 0.126 | 2.700      | 3.200  |
| L      | 0.496      | 0.535 | 12.600     | 13.600 |
| N      | 0.092      | 0.108 | 2.340      | 2.740  |
| Q      | 0.124      | 0.140 | 3.150      | 3.550  |
| ΦP     | 0.118      | 0.130 | 3.000      | 3.300  |

NOTE:

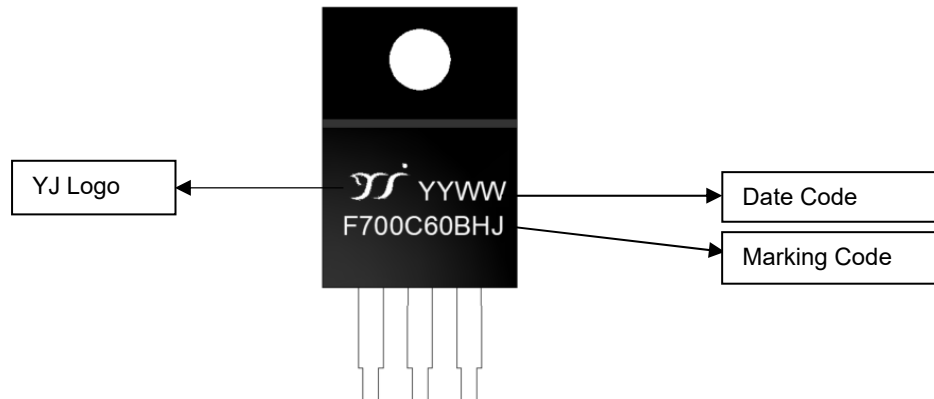
- 1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- 2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.





## YJF700C60BHJ

### ■ Marking Information



**Note:**

1. All marking is at middle of the product body
2. All marking is in laser printing
3. F700C60BHJ is marking code, YYWW is date code, "YY" is year, "WW" is week
4. Body color: Black



## Disclaimer

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