

Dual N-Channel Enhancement Mode Field Effect Transistor

Product Summary

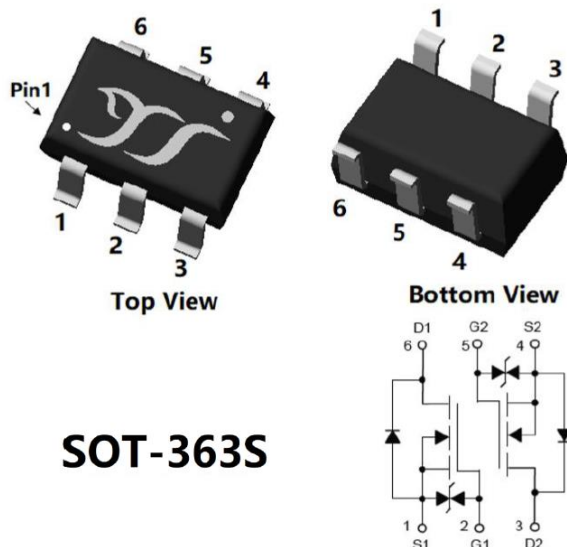
- V_{DS} 30V
- I_D 0.5A
- $R_{DS(ON)}$ (at $V_{GS}=4.5V$) $< 500m\Omega$
- $R_{DS(ON)}$ (at $V_{GS}=2.5V$) $< 700m\Omega$
- $R_{DS(ON)}$ (at $V_{GS}=1.8V$) $< 1100m\Omega$
- ESD Protected Up to 2KV(HBM)

General Description

- Voltage controlled small signal switch
- Low input Capacitance
- Fast Switching Speed
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Battery operated systems
- Solid-state relays
- Direct logic-level interface:TTL/CMOS



SOT-363S

Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage			V _{DS}	-	30	V
Gate-source Voltage			V _{GS}	-12	12	
Continuous Drain Current (Note 1,2)	Steady-State	T _A =25°C,V _{GS} =10V	I _D	-	0.5	A
		T _A =100°C,V _{GS} =10V		-	0.3	
Pulsed Drain Current	T _A =25°C,t _p ≤10μs		I _{DM}	-	4	
Maximum Body-Diode Continuous Current	T _A =25°C		I _S		0.3	
Total Power Dissipation (Note 1,2)	Steady-State	T _A =25°C	P _D	-	0.304	W
		T _A =100°C		-	0.12	
Junction and Storage Temperature Range			T _J , T _{STG}	-55	150	°C

Thermal Resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	Steady-State	$R_{\theta JA}$	-	410	$^{\circ}C/W$

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJL6303ADWJK	F2	03K	3000	30000	120000	7" reel



YJL6303ADWJK

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA, T _j =25℃	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V, T _j =25℃	-	-	1	μA
		V _{DS} =30V, V _{GS} =0V, T _j =150℃	-	-	100	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±12V, V _{DS} =0V, T _j =25℃	-	-	±10	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA, T _j =25℃	0.5	1	1.5	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =0.5A, T _j =25℃	-	330	500	mΩ
		V _{GS} =2.5V, I _D =0.3A, T _j =25℃	-	440	700	mΩ
		V _{GS} =1.8V, I _D =0.1A, T _j =25℃	-	700	1100	mΩ
Diode Forward Voltage	V _{SD}	I _S =0.25A, V _{GS} =0V, T _j =25℃	-	0.78	1.2	V
Gate Resistance	R _G	f=1MHz, T _j =25℃	-	205	-	Ω
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1MHz, T _j =25℃	-	35	-	pF
Output Capacitance	C _{oss}		-	5.9	-	
Reverse Transfer Capacitance	C _{rss}		-	3.8	-	
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =4.5V, V _{DS} =5V, I _D =0.5A, T _j =25℃	-	0.55	-	nC
Gate-Source Charge	Q _{gs}		-	0.12	-	
Gate-Drain Charge	Q _{gd}		-	0.11	-	
Reverse Recovery Charge	Q _{rr}	I _F =0.5A, di/dt=100A/μs, V _{GS} =0V, V _R =10V, T _j =25℃	-	1.5	-	nC
Reverse Recovery Time	t _{rr}		-	11	-	ns
Turn-on Delay Time	t _{D(on)}	V _{GS} =4.5V, V _{DS} =5V, I _D =0.5A, R _{GEN} =50Ω, T _j =25℃	-	4.1	-	ns
Turn-on Rise Time	t _r		-	3.2	-	
Turn-off Delay Time	t _{D(off)}		-	28	-	
Turn-off Fall Time	t _f		-	9	-	

Note:

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. The value of $R_{\theta JA}$ is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of $150^\circ C$. The value in any given application depends on the user's specific board design.

■ Typical Electrical and Thermal Characteristics Diagrams

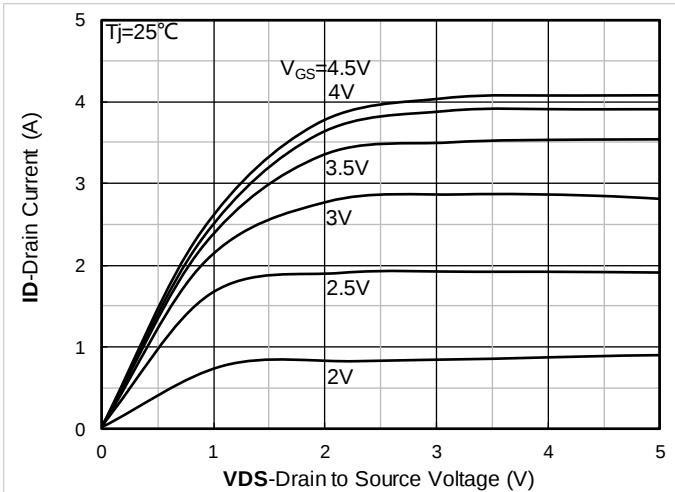


Figure 1. Output Characteristics; typical values

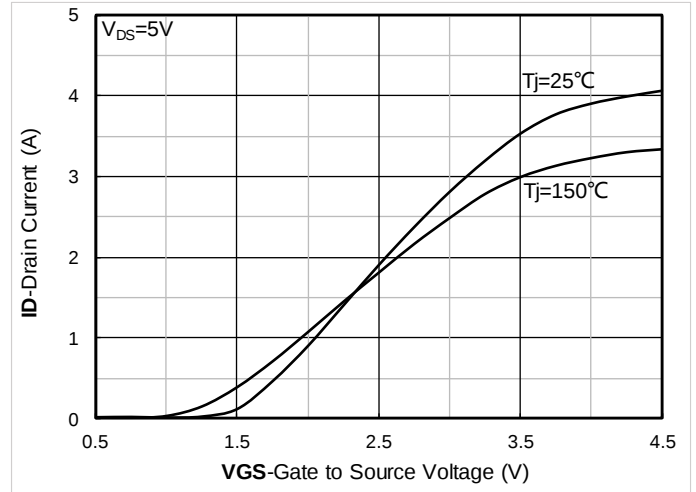


Figure 2. Transfer Characteristics; typical values

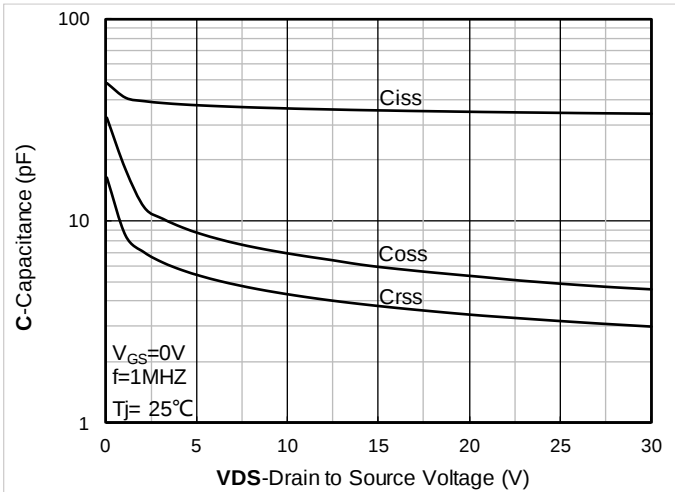


Figure 3. Capacitance Characteristics; typical values

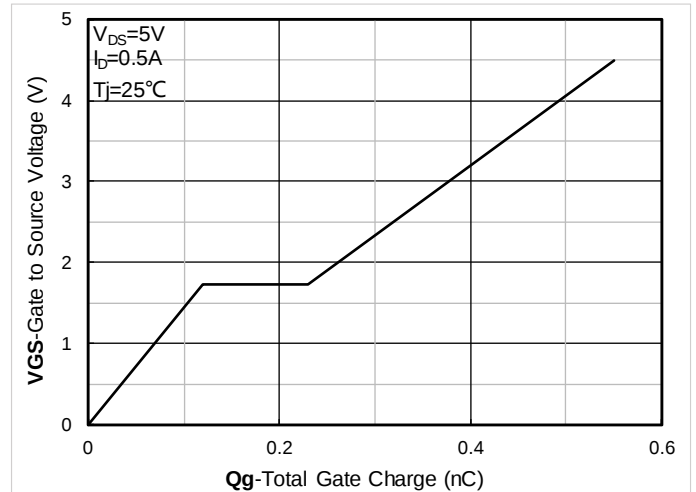


Figure 4. Gate Charge; typical values

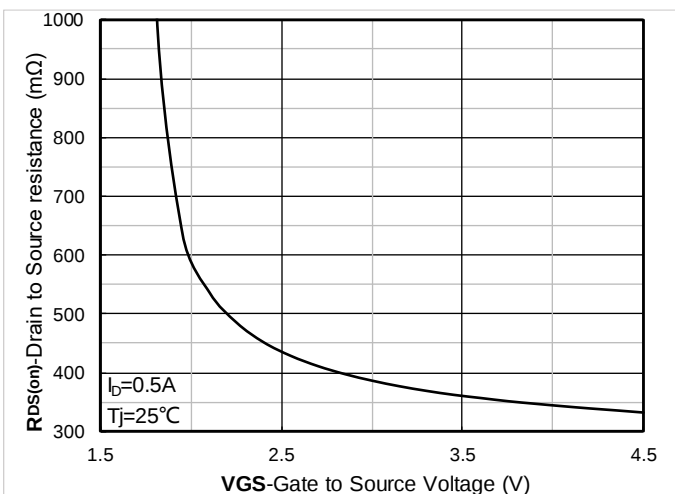


Figure 5. On-Resistance vs. Gate to Source Voltage; typical values

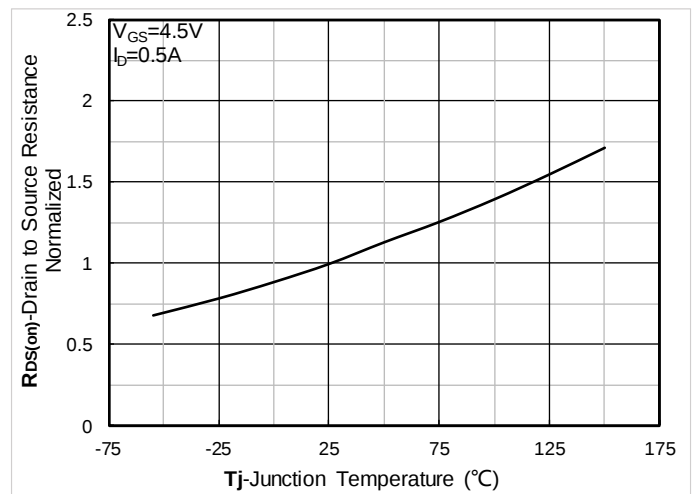


Figure 6. Normalized On-Resistance

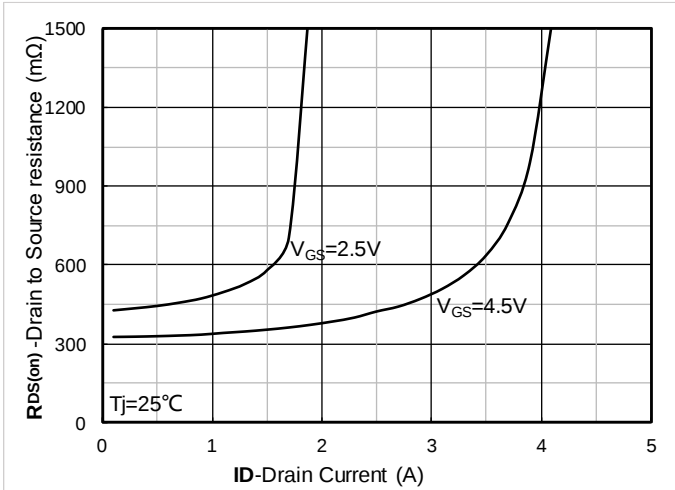


Figure 7. $R_{DS(on)}$ vs. Drain Current; typical values

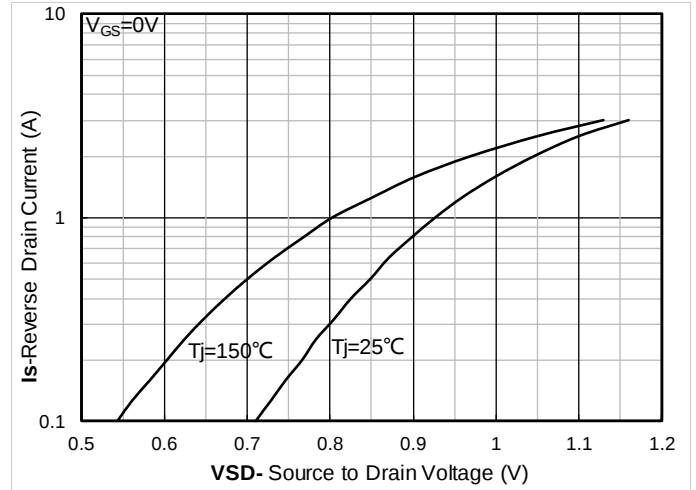


Figure 8. Forward characteristics of reverse diode; typical values

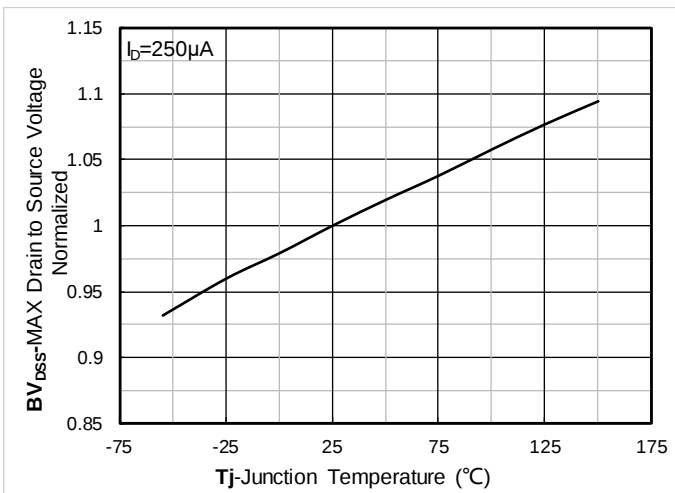


Figure 9. Normalized breakdown voltage

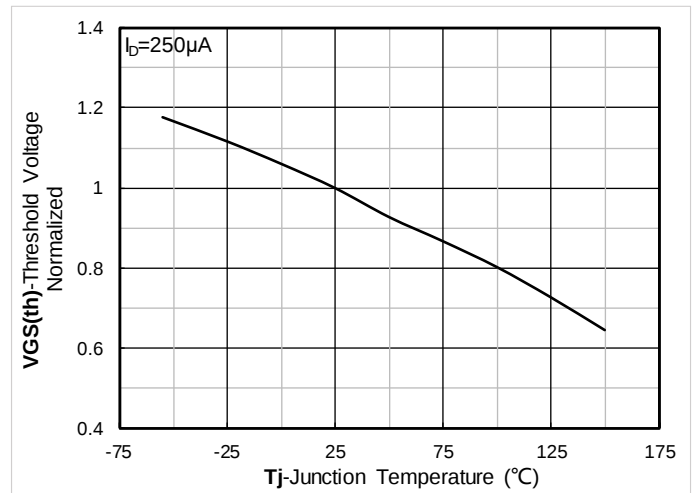


Figure 10. Normalized Threshold voltage

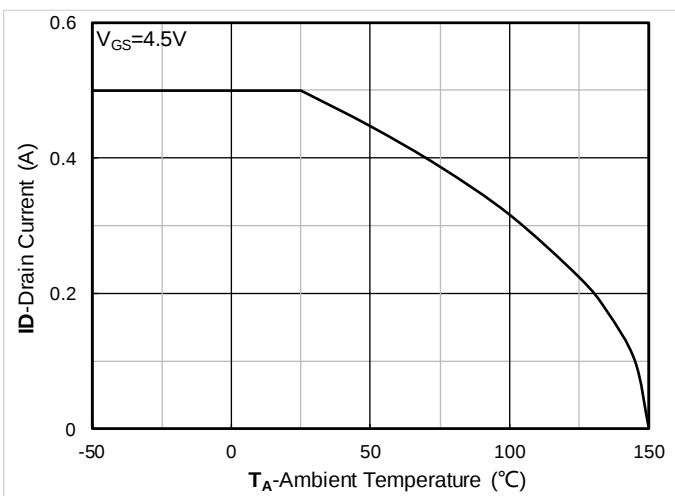


Figure 11. Current dissipation

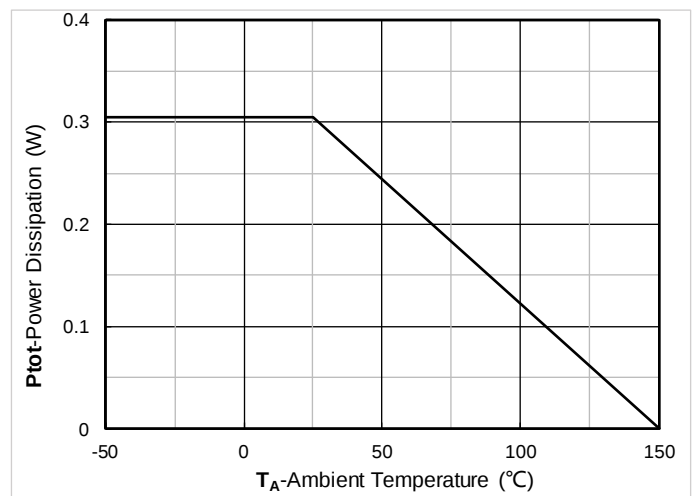


Figure 12. Power dissipation

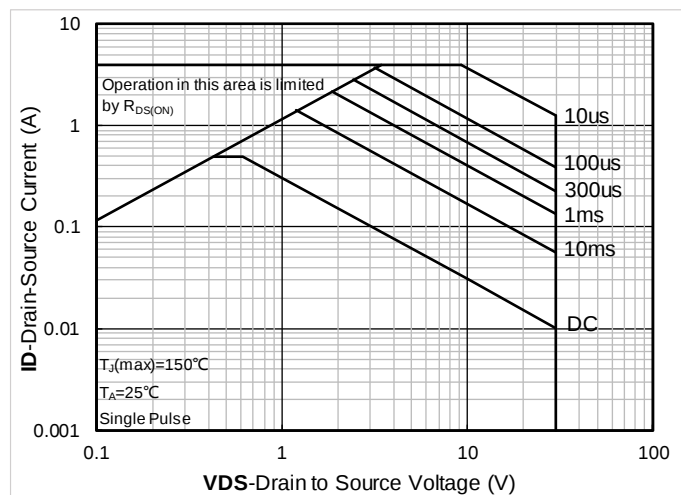
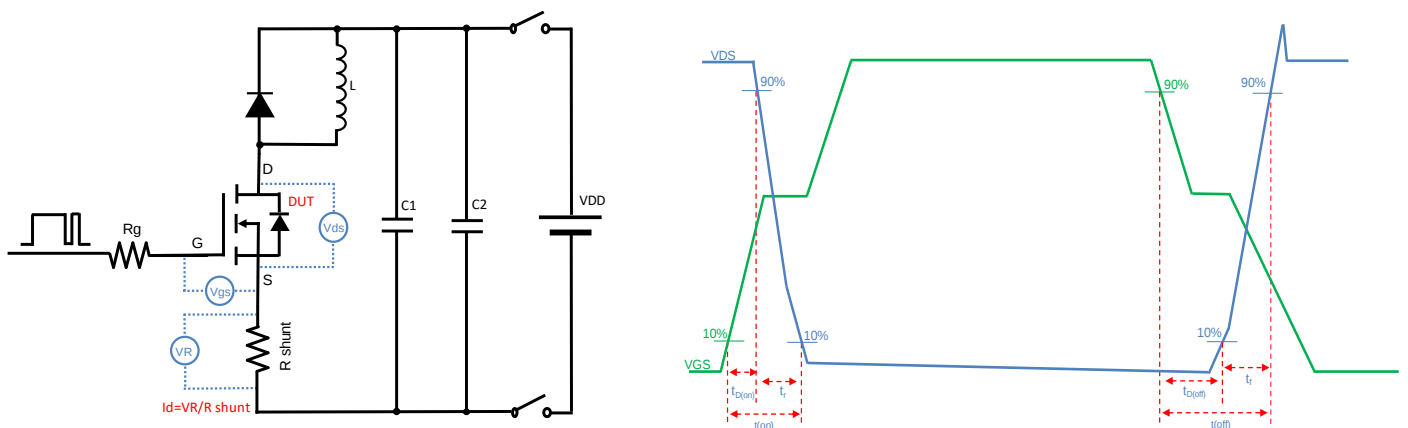
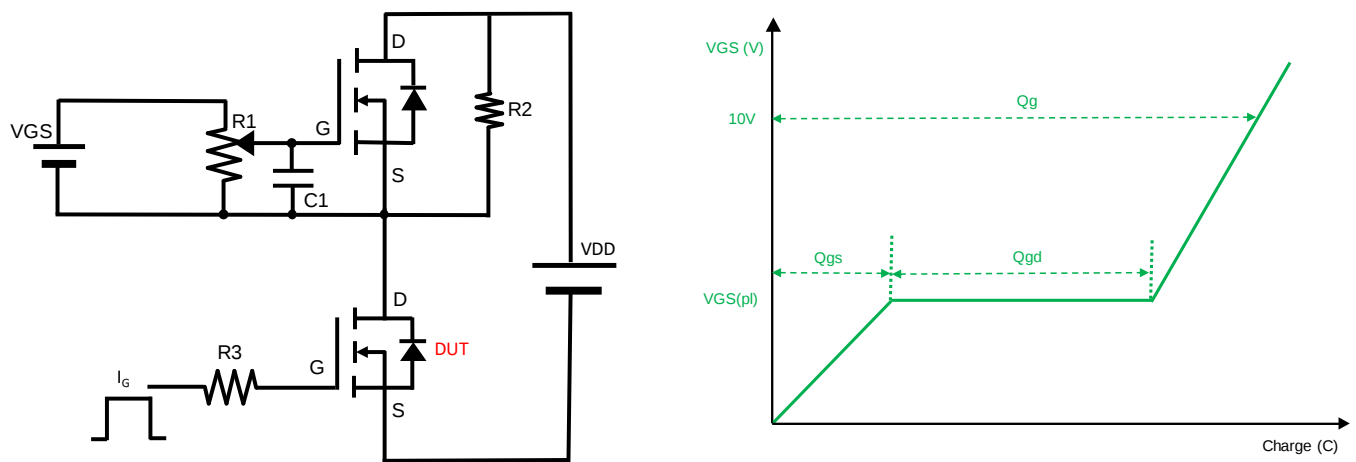
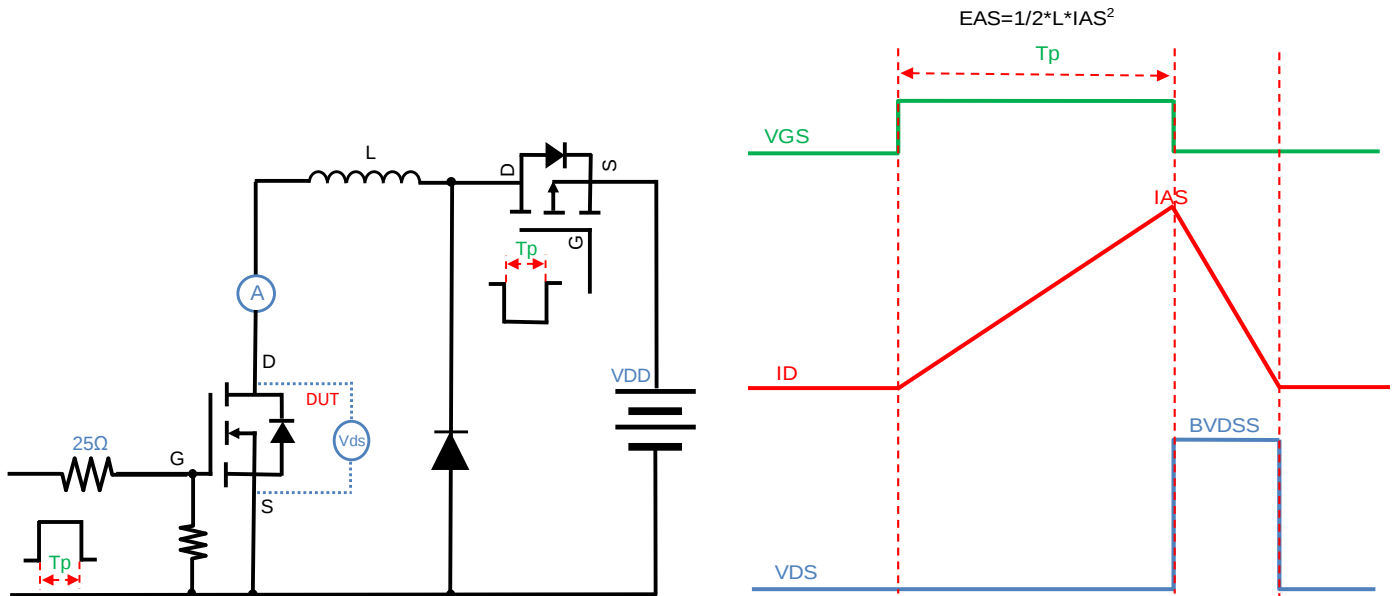


Figure 14. Safe Operation Area

■ Test Circuits & Waveforms



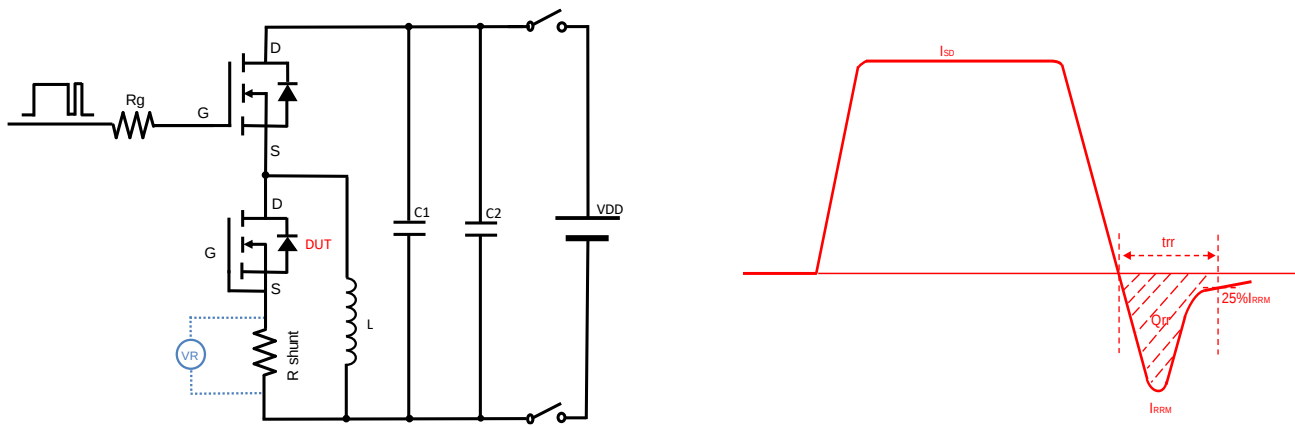
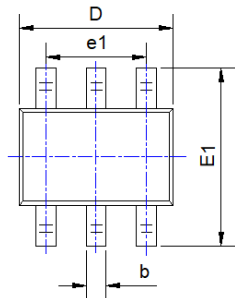


Figure D. Diode Recovery Test Circuit & Waveform

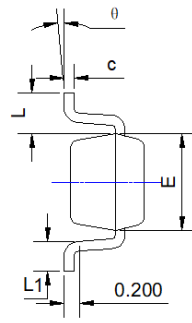


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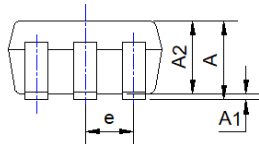
■ SOT-363S Package information



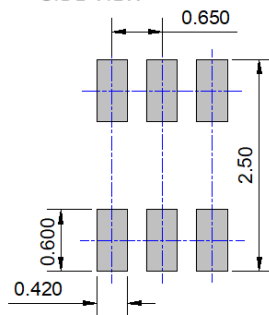
TOP VIEW



SIDE VIEW



SIDE VIEW



UNIT: mm

SUGGESTED SOLDER PAD LAYOUT

SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.006	0.014	0.150	0.350
c	0.004	0.010	0.100	0.250
D	0.071	0.087	1.800	2.200
E	0.045	0.053	1.150	1.350
E1	0.079	0.087	2.000	2.200
e	0.026TYP		0.650TYP	
e1	0.047	0.055	1.200	1.400
L	0.021REF		0.525REF	
L1	0.008	0.016	0.200	0.400
θ	0°	8°	0°	8°

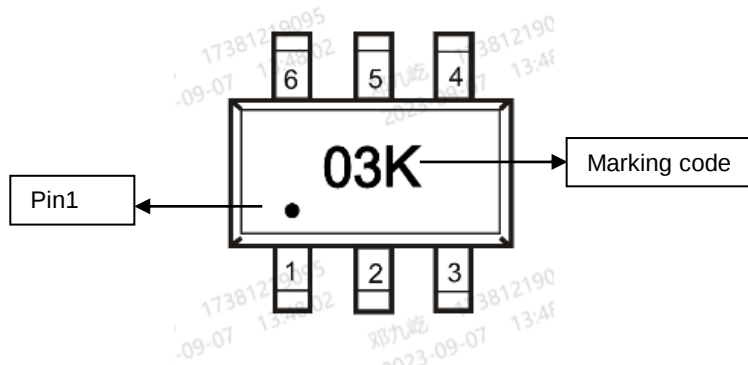
NOTE:

1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.

2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.

3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

■ Marking Information



Note:

1. All marking is at middle of the product body
2. All marking is in laser printing
3. 03K is marking code
4. Body color: Black



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